



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Youngmin Kim, et al.
Serial No: 10/028,002
Examiner: Jesse A. Fenty
Filed: 12/20/2001
For: HIGH PERFORMANCE PNP BIPOLAR DEVICE FULLY COMPATIBLE WITH CMOS
PROCESS

#4/Election
RECEIVED 1/4/03
Docket No. PI-31160
Conf. No. 2001-01-25
Tech. No. 2001-01-25
Art Unit: 2815

ELECTION

Assistant Commissioner for Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service with sufficient
postage as First Class Mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, DC
20231 on 1-2-03

Ann Trent
Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement
mailed December 2, 2002.

Applicants hereby elect to pursue Group I of Claims 1-8, drawn to a semiconductor
device, without traversing the Examiner's restriction requirement.

Respectfully Submitted,

Peter K. McLarty
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